OPETON	EV182663766 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE 8/I.D.S
CI 2 3 Table 1997	IN THE UNITED STATES PATENT AND TRADEMARK OFFICE 8/I.D.S
* Application	Serial No
Filing Date	Serial No
Inventor	Jigish D. Trivedi, et all 🖰 📉 🔌
Assignee	Micron Technology, Inc.
Group Art U	Jnit
Examiner	
Attornev's [Docket No

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Integrated Circuitry (As Amended)

Respectfully submitted,

D. Brent Kenady

Reg. No. 40,045

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